INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

| Application Number | | 10675778 |
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| Filing Date | | 2003-09-30 |
| First Named Inventor DeW | | tt, Jr. et al. |
| Art Unit | | 2183 |
| Examiner Name Cody | | , Dillon J. |
| Attorney Docket Numb | er | AUS920030480US1 |

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| 1 CPU Cache*, Wikipedia, pp. 1-14 retrieved 11/106 http://en.wikipedia.org/wiki/CPU_cache | | | | | | | | | | | |
| 2 AMM/CNS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96 | | | | | | | | | | | |

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